"A TRANSPORTABLE 500 kV/0.9 ns-RISE TIME NEMP GENERATOR"

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NISM OF MICROWAVE ABSORPTION MEMBRANES

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INTRODUCTION

To simulate a Nuclear EMP corresponding to the new demands, a pulser must be able to generate a pulse with a very short rise time corresponding to frequencies in the Giga Hertz region within the working volume of the NEMP simulator. In order to do so, the pulser should have very small dimensions, and act more or less like a point source. Otherwise non-TEM modes are already exited by the pulser itself.

The described generator therefore has cross-sectional dimensions of only 10 cm at the connection of the switch with the wave launcher. The oil-filled fast output circuit furthermore consists of a strip-line capacitor and a SF6-pressurized rail-gap switch.

This fast pulse circuit is driven by a 500 kV/5 ns rise time pulse generator. A 20 feet, nonmetallic, container houses the whole generator system.

Constructing such very fast generators learns that there are many limitations, such as minimum dimensions, insulation, series inductance and speed of the switch. So each part of the system must be designed very carefully, nearly up to the limits of what is physically possible. The described fast circuit has been made in a strip-line structure from the beginning to the end. Figure 1 explains this.

To match the 90Ω wave launcher to the generator, the characteristic impedance of the strip-line structure is kept 90 Ω constant up to the output electrode of the switch. The input electrode is connected to the strip-line capacitor via a very short, low inductance conductor.

The rise time of the generated pulse depends strongly on the fast circuit inductance, the characteristics of the output switch, the way of driving the circuit, and the quality of the output transition.

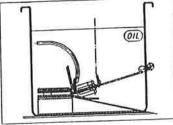


Fig. 1: Fast pulse circuit in strip-line structure.

THE SWITCH

An elliptical curved, 6 cm wide stainless steel rail-gap in SF6, in self breakdown mode, was chosen. The actual closing of this output switch is determined by two mechanisms:

a) Statistical time-lag. This is the time needed to form a free electron in the gap volume. b) Formative time-lag. This is the time to build up the discharge current to its maximum value.

These time-lags can be decreased considerably by operating the switch under over-voltage conditions, by driving the circuit with a 5 ns-rise time pulse generator (type 8080 of Physics International Company) via a resistor. For a short time this stressing voltage can be much higher than the static breakdown voltage of the gap.

The rise time related to the switch can be calculated from:

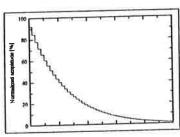
$$t_{gap} = 2.2 \cdot 88 \left(\frac{\rho}{\rho_o}\right)^{1/2} \cdot \left(Z_c + Z_l\right)^{-1/3} \cdot E^{-4/3}$$
 [Instance]

Where ρ/ρ_0 is the ratio of the SF6-gas density to that of air, E is the electric field in the gap in MV/m, $Z_c + Z_1$ is the total circuit impedance. For a single gap (0.9 cm at 10 bar of SF6) and a voltage of 500 kV, the rise time calculated from the upper equation is 0.56 ns. For a total rise time of 0.9 ns, the inductance-related rise time must be not larger than 0.7 ns. This means that the total circuit inductance must be equal or less than 30 nH; this can only be realized in a coaxial structure. Therefore the strip-line shape has been chosen. For a further decrease of this inductance the rail-gap principle was applied. By applying the charging voltage, of the stripline capacitor, to the same point where the switch is connected, some time is saved by prestressing the gap.

THE STRIP-LINE CAPACITOR

It is formed by aluminium ground and top plates, with a specially shaped 1) centre conductor plate insulated by two plates of dielectric material. Considered as a piece of transmission line, the output-pulse amplitude depends on the characteristic impedance Z_c of this line. The lower Z_c, the higher the pulse amplitude, because of the higher reflection coefficient, when the charged strip-line is switched to the load Z_1 (90 Ω), see also figure 2. For a low Z_c the dielectric insulator must have a high ε and must be thin and/or the line must have a large width. These data are in contradiction with high voltage insulation and speed. So, eventually 1.2 cm thick polythene was chosen with a moderate ε_r (2.35) but with a very low loss for high frequencies ($tg\delta = 0.0001$) and high dielectric strength (700 kV/cm). A sandwich construction was used to half Z_c to about 4 Ω .

Once the switch closes, nearly 92% of the voltage is reflected to the open rear of the strip-line. So a stair-case like output pulse is generated (figure 2), each step having 92% of the amplitude of the previous one, and a length of less than 2 ns. Notice that the pulse duration is a function of the reflection coefficient, and the length of the strip-line. Because the pulse duration of the 8080 driver is much longer than the wanted pulse, the measured output pulse has a longer tail than the calculated pulse (figure 2). This could be improved by pulse compression technics.



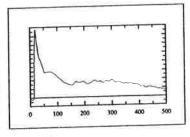
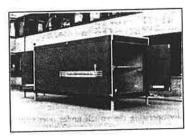


Fig. 2: The generated output pulse; calculated 1) and measured 2).



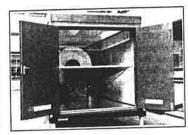


Fig. 3: The complete generator installed in a transportable non-metallic container.

Producing High-Voltag

T.Shiku Fuji Elect 7, Yawata-kaigandor

I.Introduction

Pulsed power sources to generate researches in different fields of generators have been designed wh range of about a few MV and pulse generators are very expensive and of Magnetic Compression explosive advantages, because they are ec weight. In this paper, more empha MC-generators to provide the pulse The MC-generators are usually low a wide variety of low impedance l could take advantage of the posit conditioning systems were available In the earlier researches, some pu for that system. However, such dev generators are used. In this paper, we show the calcu

results of more compact syst!

produce high voltage-pulses.

[].Calculation Circuit

Figure 1 shows the calculation c for high voltage generation. In circuit, initial current flows thr generator inductance and interrup off. If SWor, and closing switch is operated, then currer interrupted by SWor, voltage is at entrance of transmission line This induced voltage propagates end of line, the voltage will t twice at the end becaus missmaching.

In our calculation, the interrupter, such as opening switch, and closeing switch is assumed to variable resistance that the resistance changes linealy.

M.Results

Figure 2 shows the relationship between final resistance Roll of interrupter and capacitor voltage without T.L.. The output voltage increases as resistance becomes large. For high performance switches, such as plasma compression switch using explosives, the rate of resistance

The plate forms about half a circle, to have equal travelling times, back and forward, of the reflected voltage step to the point the switch input-electrode is connected.

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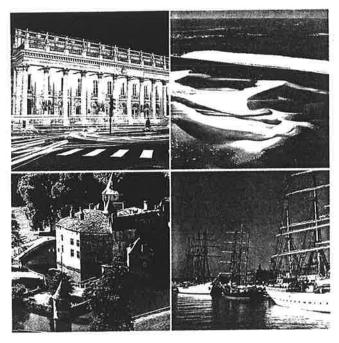
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