

Harmonic Distortion and Oscillatory Voltages and the Role of Negative Impedance

P.J.M. Heskes, J.M.A. Myrzik, *Member, IEEE* and W.L. Kling, *Member, IEEE*

Abstract-- This paper focuses on two power quality effects that are noticed with power electronic appliances. One effect is a harmonic oscillation by inverters of photo voltaic systems because they bring a high parallel capacitance to the grid and negative output impedance. The other effect is a sub-harmonic oscillation due to the negative differential impedance of constant power loads.

This paper discusses both effects and proposes counter measures to mitigate these effects. For harmonic oscillations due to photo voltaic inverters, an extra feature for these inverters is discussed. For the sub-harmonic oscillatory voltages due to appliances with negative differential impedances, a solution can be found in the voltage control systems of the power generators connected to the network.

Index Terms--Constant Power Load, Harmonic Interaction, Negative Differential Impedance, Negative Impedance, Oscillatory Grid Voltage, Photo Voltaic Inverter, Power Quality.

I. INTRODUCTION

TODAY'S appliances that are equipped with a power electronic converter can bring a negative differential impedance behaviour, which is caused by the converter control system. This kind of impedance can bring an oscillatory grid voltage in a frequency range below the fundamental frequency [01], [02]. Other voltage instability effects in the grid are noticed with inverters for photo voltaic systems. They can bring negative absolute impedance to the grid in the harmonic frequency range. This kind of impedance can cause resonances in a frequency range above the fundamental frequency, which can provoke harmonic voltages in local distribution grids [03].

The work presented in this paper is part of the research project 'Voltage quality in future infrastructures' ('Kwaliteit van de spanning in toekomstige infrastructuur (KTI)' in Dutch), sponsored by the Ministry of Economics Affairs of the Netherlands.

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This paper focuses on both effects and gives a description of the role of the network impedance herein. A simplified model is given to illustrate the harmonic interaction inside a local distribution system. Counter measures to control both effects are proposed. For harmonic distortion in local distribution systems due to resonances an active approach is discussed, that makes use of a combination of extra features (ancillary services) of power electronic inverters of distributed generators. For the low frequency oscillatory voltages caused by negative differential impedances, a solution can be found in voltage control systems of the involved generators.

An oscillatory grid voltage due to the negative differential impedance of constant power loads is a phenomenon that already showed up in small micro grids like electricity systems on board of ships, where generators became instable because their control systems were responding on a large share of constant power loads [04]. Beside the rapidly growing numbers of constant power loads, today's ideas of future electricity systems propagate the possibility of isolating local distribution systems in critical situations [05]. The chance that a constant power load takes the overhand is much higher in these small isolated systems; therefore this form of instability will firstly become a problem in small grids. However it might also show up in larger electricity grids when the constant power load share continues to grow.

II. IMPEDANCE AS TRANSFER FUNCTION

In general it can be said that harmonic currents are transferred into harmonic voltages via network impedances. In a local distribution system, all cable parts and the transformer have impedance. Especially for the harmonic frequency range the dominating impedance in this is the leakage inductance of the transformer, therefore in a more simplified model of a distribution system, the cable impedances can be neglected. With this assumption, the network can be simplified to the model of Figure 1, just for understanding the system of harmonic interaction.

With this model it easily can be seen that voltage drop over the network impedance will affect the voltage of all the loads connected to the Point of Common Coupling (PCC). This counts for the fundamental as well as for harmonics. The load in the model is a non-linear load and modeled as a linear load with harmonic current sources in parallel [06]; only one harmonic source is depicted.

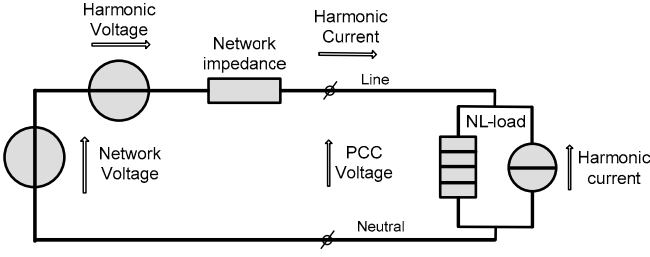


Figure 1, A simplified distribution network model.

Harmonic currents from non-linear loads will distribute itself over all impedances in the network, and the lowest impedance path will have most of the share. The part of the harmonic currents that is flowing through series impedances, like the network impedance of Figure 1, will be the main cause for the transfer into harmonic voltages at the PCC.

Harmonics from non-linear loads outside the network can be modeled as an added voltage source in series with the fundamental voltage [07] (see Figure 1). The network impedance in this figure is the distribution transformer impedance plus the impedance of the next higher network where the disturbing load is located; the latter impedance is lower in magnitude than that of the distribution transformer. In practice this so called background distortion can be significant with large non-linear loads.

In case of background voltage distortion on the fundamental, all loads in the low voltage network will draw current from this harmonic voltage source. This current then will flow through the network impedance, i.e. the distribution transformer, which can bring a number of unwanted effects to this component.

Impedance can be seen as a transfer function from current to voltage. To get more insight in possible voltage instability with this, a general equation for a linear transfer function in the Laplace domain is given in (1).

$$H(s) = k \frac{\prod_{p=1}^m (s - z_p)}{\prod_{q=1}^n (s - p_q)} \quad (1)$$

Wherein $s = \lambda + j\omega$ is the complex frequency variable, z_p the zeros and p_q the poles of the transfer function. Due to the fact that physical systems cannot increase the response to infinite with a frequency rising to infinite, the order of the numerator must be equal or less than that of the denominator, so $n \geq m$ in (1).

Poles of this impedance transfer function that lie in the right half-plane of its PZ-map indicate instability. The voltage instability effect that can show up from this transfer function $H(s)$, is that the transition from one output state to a new state, will not be damped to zero, but starts with an oscillation. A remarkable fact is that if the transfer function is taken the opposite way, from voltage to current,

impedance will be turned into admittance, and poles will turn into zeros and the other way round, zeros will turn into poles. With this a stable system can become unstable. An example of this is the fluorescent lamp, that cannot be driven in a stable way directly (without a ballast) from a voltage source, but on the other hand can be driven in a stable way directly from a current source [08].

A. Negative impedance

Impedance is called to be negative, when the real part of the complex impedance is negative. Assume that there are appliances in the grid that show a negative impedance value, then these particular impedances can reduce the damping in the system because poles are shifted towards the positive real half-plane of the PZ-map. If the number of this kind of impedances rises significantly, poor damped resonances might occur. This phenomena can show up when the grid voltage level goes from one state to a new state, as can be seen further in this paper.

III. ABSOLUTE AND DIFFERENTIAL IMPEDANCE

Impedance can turn out to be absolute or differential, depending on the way the system is stimulated. The absolute impedance of a grid connected appliance is the operating point voltage, divided by the current through it. The differential impedance is the impedance that counts for deviations round an operating point. In practice the operating point is the grid fundamental voltage and the deviations can be a slow variation like a flicker distortion, a sub-harmonic or an oscillation.

Non-linear grid connected appliances can present a different impedance for each operating point. Appliances that can be specified with only one impedance figure for all operating points are pure linear components without saturation effects. For example, a pure resistor has the same impedance for all frequencies in all operating points, where the absolute and differential impedance is just the resistor value. Generally this is not the case; even an incandescent lamp is not linear due to the temperature dependence of the filament resistance. This temperature effect is a slow process; therefore its differential impedance differs from the absolute impedance at slow varying voltage levels.

Loads or sources connected to the grid with power electronic converters have the potential to have negative absolute, as well as negative differential impedance, for certain frequencies. This can lead to voltage instability [01], [09].

IV. NEGATIVE ABSOLUTE IMPEDANCE

The V-I plot of a negative absolute impedance is shown in Figure 2, however this cannot exist with a single physical component, also an energy source is required. Inverters for Distributed Energy Resources (DER) can show a negative absolute impedance in a certain frequency range [10].

A. Inverters

Inverters can have a negative absolute output impedance in the harmonic frequency range that can reduce damping in the grid and beside this, they can cause resonances for even lower than the 25th harmonic, due to the high parallel capacitance at the feeders. The negative absolute impedance in the harmonic frequency range is an effect that depends on the inverters control system [09].

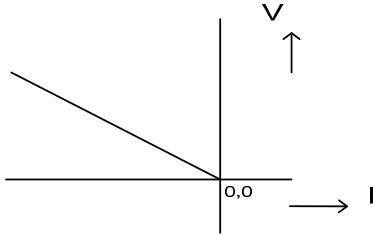


Figure 2, A negative absolute impedance.

1) Parallel capacitance and grid resonances

Because today's modern home appliances and small inverters for DER bring in more and more parallel capacitances to the grid, the effect on the total grid impedance in the low voltage distribution grid is significant. Although these capacitances are small for home appliances, due to the large and still increasing numbers, the total parallel capacitance in distribution grids can rise to a high level. In today's practice the EMI filter capacitances of DER inverters are much higher than those of modern home appliances of comparable power size. Installing a DER inverter of 2kW or more in a dwelling, can triple the total capacitance at the Point of Connection (PoC).

Parallel capacitances and inductive grid impedances can come into resonance. This resonance can let the impedance around the resonance frequency, rise to a much higher level. In this way harmonic currents that lie in the same frequency range can be transferred into problematic high harmonic voltages. These resonances are a real threat for power quality. Beside the resonance phenomenon, an inverter might also reduce the damping of this resonance by bringing in a negative impedance [03].

B. Laboratory tests on an inverter load

To estimate the potential to reduce damping in the grid by a small single-phase PV inverter, laboratory measurements were done that show the real part of the complex conductance. The first 50 harmonics were examined on a small single-phase PV inverter with an output power less than 300W. The inverter is equipped with fast switching power transistors and a high frequency ferrite transformer.

In Figure 3, the measured G_n/G_{ref} is presented for a number of harmonics. G_n is the real part of the harmonic conductance and G_{ref} is a constant to normalise the conductance. G_{ref} depends on the nominal inverter power.

In Figure 3 it can be seen that G_n/G_{ref} is negative up to the 25th harmonic, this means that the inverter reduces damping in the grid which can provoke oscillations in this frequency

range. G_n/G_{ref} is strongly positive from the 25th up to the 50th harmonic; this means that the inverter brings extra damping to the grid in this frequency range, compared to a linear load.

This behaviour strongly depends on the inverter's control system and is not a common practice; another type of inverter can show a different behaviour [11].

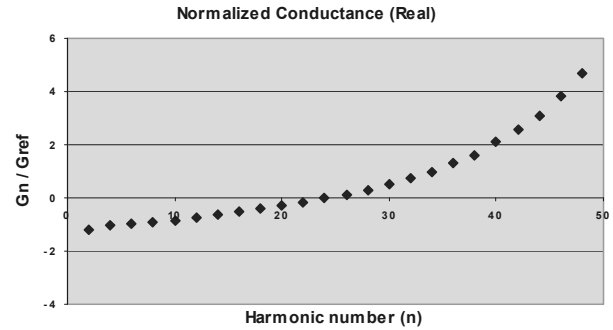


Figure 3, Real part of the inverters conductance (normalized).

C. Harmonic voltage distortion due to resonances

An example of a practical situation is taken from the Bronsbergen holiday park in Zutphen, the Netherlands [12]. The situation there is as follows:

- one 400 kVA transformer
- 108 PV inverters of 2.5kW
- 210 cottages.

The peak of the parallel resonance can be expected at the 12th harmonic, this can be calculated with the estimated figures below:

- | | |
|------------------------------------|--------------|
| ▪ 400 kVA transformer leakage ind. | 50 μ H |
| ▪ 108 pcs. 2.5kW PV inverters cap. | 810 μ F |
| ▪ 210 cottages parallel cap. | 630 μ F. |

Figure 4 gives a measurement result of the voltage of one of the substation feeders of the Bronsbergen holiday park. Two fairly high harmonic levels can be noticed at the 11th and 13th harmonic, a resonance according to the estimation could be the cause of this.

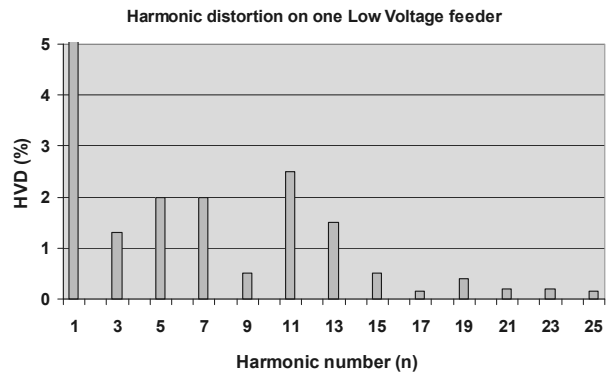


Figure 4, Harmonic voltages of one of the substation feeders in the Bronsbergen holiday park.

D. Reduction of grid resonances and harmonics

In this investigation, the proposed reduction of grid resonances and harmonics is a combination of two measures, firstly shifting the resonance out of the harmonic area of concern; that is below the 40th harmonic, and secondly damp the resonance peak to a lower level. The second measure will also lead down harmonic currents.

1) Shifting of resonances

Shifting resonances is a method to avoid resonances in the frequency area of concern. In situations where distribution networks are not having large capacitances in parallel to the load, the possible resonances are in general in higher frequency ranges than the area of concern. In this higher frequency range harmonics will not propagate far in the network, because of the damping effects of cables and transformers [13]. The solution lays thus in reducing the high parallel capacitance of inverters for DER. In Figure 5 the effect of a reduced capacitance can be seen on a calculated Bode impedance plot.

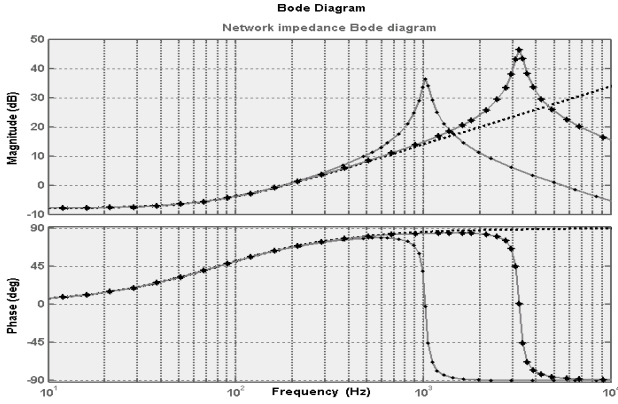


Figure 5, Bode impedance plot with a parallel resonance, dotted line: Unloaded network, line with dots: High par. cap. inverter load, line with asterisk: Low par. cap. inverter load.

To reduce the parallel capacitance, an extra control system can be added to the inverters prime control system, that virtually reduces this capacitance for all frequencies lower than a certain harmonic number [14]. With this, the EMI performance of the output filter is not affected.

2) Damping of resonances

The second measure for the reduction of resonances is to add an extra control loop to the inverter which gives the inverter a resistive behavior for the harmonic frequency range. This will bring extra damping to resonances in the network [15], [16]. As indicated in Figure 6, implementing the combination of both methods can be very effective for resonances.

3) Damping of harmonics

Beside the damping of resonances the inverters virtual resistive behavior brings, it also leads down harmonic currents from non-linear loads and with that reduce harmonic voltages at the PCC. There is however a contradiction with the needed measure for harmonics from outside the distribution network, the so called background

harmonics, therefore the value of this virtual resistor must be limited to restrict harmonic currents through the distribution transformer, cables and/or lines [07], [17].

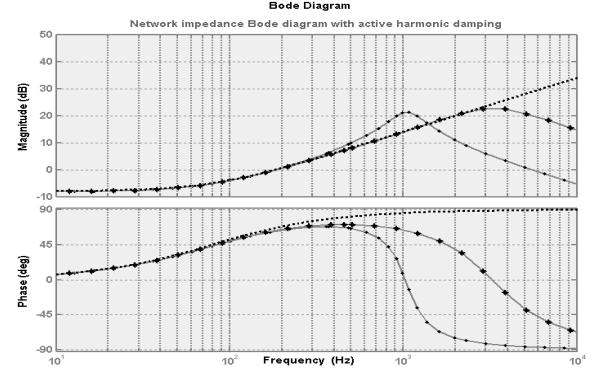


Figure 6, Bode impedance plot with a damped parallel resonance.

V. NEGATIVE DIFFERENTIAL IMPEDANCE

Loads with power electronic converters that draw a constant power from the grid at various grid voltage levels, are constant power loads. These kind of loads have the potential to show a negative differential impedance for certain frequencies.

A. Constant Power Load

Just as with non-linear loads in general, the impedance of a constant power load depends on the applied voltage. In practice, a lot of home appliances are loads with AC to DC converters inside, feeding an internal part that works on a constant direct voltage. The control system of these AC to DC converters regulates the direct voltage U_{dc} in such a way that the voltage is independent of the applied grid voltage U_{ac} . This implies that the AC power remains constant if the grid voltage varies. If the grid voltage increases, the grid current will decrease and also the other way round, if the grid voltage decreases, the grid current will increase. This behaviour brings negative differential impedance and this can lead to voltage instability [01].

In Figure 7, the V_{RMS} - I_{RMS} curves of two typical loads are given. The left side figure could be that of an incandescent lamp, where the dashed line is the absolute impedance for a certain operating point that depends on the applied RMS voltage, and the straight solid line the positive differential impedance in that operating point. The right side figure could be that of a power supply, which shows a negative differential impedance.

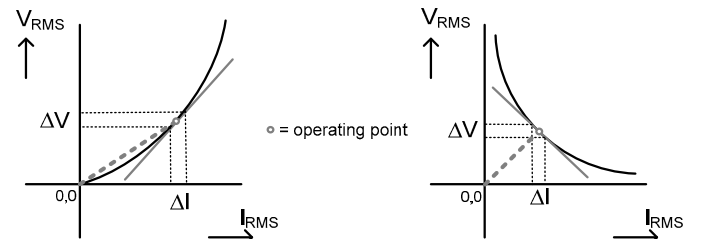


Figure 7, V_{RMS} - I_{RMS} curves of two typical loads.

To show the negative differential impedance effect, the operating point of a constant power load must be changed. This change happens when the amplitude of the grid voltage is fluctuating slowly, in a way that the appliance is able to follow this variation with a changing operating point. This can happen when a low frequency distortion voltage modulates the grid fundamental voltage.

Formula 4 below gives the situation where the fundamental voltage u_f (2) is modulated with a small, low frequency distortion voltage u_d (3):

$$u_f = \hat{u}_f \cos(\omega_f t) \quad (2)$$

$$u_d = \hat{u}_d \cos(\omega_d t) \quad (3)$$

Then the total modulated grid voltage u_{mod} is:

$$u_{mod} = (\hat{u}_f + \hat{u}_d \cos(\omega_d t)) \cos(\omega_f t) \quad (4)$$

From (4) can easily be seen that due to the modulation, the amplitude of the applied fundamental voltage on a constant power load can vary in time. Modulation gives a slow varying amplitude when the frequency of u_d is much lower than the frequency of u_f . This means that the operating point of a constant power load can be changed and that the negative differential impedance effect can show up.

Based on data from laboratory measurements calculations to find the differential impedance are done with the help of Matlab/Simulink software and Discrete Fourier Transformation (DFT). These calculations count for small signals, because the differential impedance only can be seen as linear for small signals around an operation point.

B. Laboratory tests on a constant power load

To measure the differential impedance of a home appliance, a PC + LCD screen load is brought to a laboratory and subjected to a modulated 230V/50Hz single-phase grid voltage from a grid simulator. A drawing of this set-up is given in Figure 8.

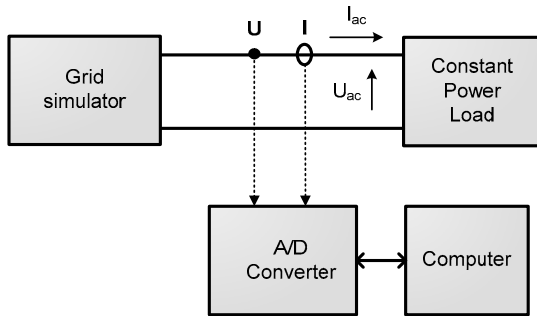


Figure 8, Measurement set-up on a constant power load.

With this set-up time series of the sampled input voltage and current are stored and analyzed via DFT in a computer with Matlab/Simulink, to calculate the differential impedance in the frequency domain. The grid voltage is modulated with a series of frequency from 0.02 Hz to 5 Hz. A Polar plot of the calculated impedance of this system is shown in Figure 9.

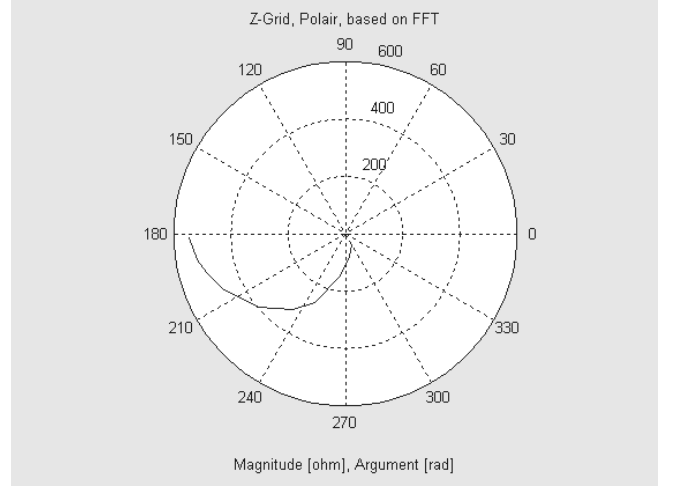


Figure 9, Polar plot of measured negative differential impedance.

Figure 9 points at a system that starts from pure resistive negative differential impedance at frequencies below 0.02Hz, towards a more capacitive-resistive system at higher frequencies. Although the polar plot does not show frequency on a scale, the differential impedance turns from negative to positive at about 4Hz. So if this negative differential impedance brings oscillations in the grid voltage, they are expected below 4Hz.

C. Oscillatory voltages

In this section an oscillatory grid voltage is simulated with the help of a Matlab/Simulink computer model. The model is build out of a generator feeding in the first place an inductive load and a resistive pre-load (see Figure 10). In the second place, after switching, the resistive pre-load is removed from the network and replaced by a combination of a resistive load and a constant power load. With this simulated set-up, the effect of various constant power load shares on voltage oscillation is analyzed.

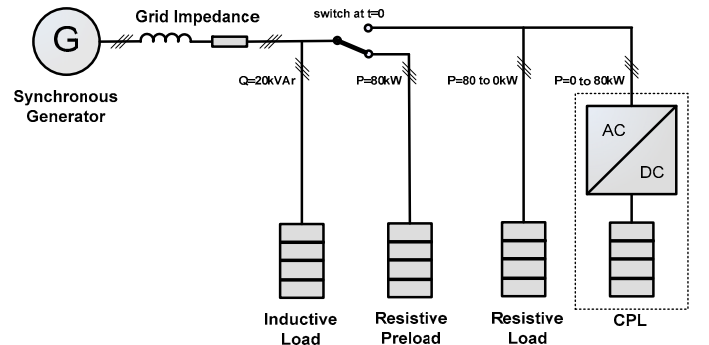


Figure 10, Generator-load model.

For the generator, a detailed model of a Diesel-Generator combination from the Matlab/SimPowerSystems library was used in the first place. To speed-up the simulations, this part was replaced by a more simplified dedicated model, with comparable results in this field of interest. The chosen inertia constant has a value of $H=1.5s$. Further the nominal generator power equals 120kVA. The generator speed is controlled by a governor system and the generator output voltage is controlled by a field winding control system.

The constant power load has an internal AC/DC converter loaded with a resistor at the DC output voltage. Although this converter is a three-phase type, an internal buffer capacitor of 3000 μ F was used in the model. The maximum power of this load is 80kW. The power of the constant power load is adjusted by changing the load on the DC output side of the converter. The loads applied to the generator do not exceed the generators maximum power level. With this, voltage collapse is not noticed, only oscillatory voltages.

In Figure 11, the line RMS Voltage as function of time is plotted with different load situations, combined to a three-dimensional plot. The generator is loaded in all cases with 20kVAR inductance. Beside this, 80kW active power is drawn from the generator, by a mixture of resistive load and constant power load, so the total active power equals 80kW for each combination. The expressed percentage of the constant power load is referred to the total load of 80kW, so 50% constant power load means that the grid is loaded with 40kW constant power load and 40kW resistive load.

Before the starting point time = 0, the grid is in a stable condition, preloaded with 80kW resistive load and 20kVAR inductive load. The resistive preload is switched off at time = 0 at the first current zero-crossing. Directly hereafter at a zero-crossing of voltage, the variable resistive part from the linear load and the constant power load are switched on. The small delay results in a step response, which shows the damping of the system.

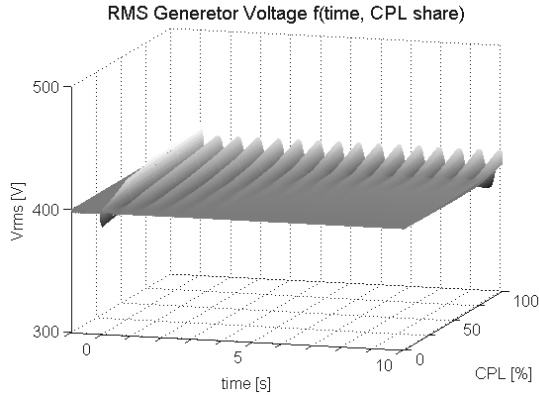


Figure 11, Effect of a constant power load share in the Generator-Load Model.

As can be noticed from Figure 11, at a constant power load share of about 85% and more, the voltage in this simulated set-up remains oscillating.

D. Reduction of oscillatory voltages

In the simulation of the previous paragraph, the voltage control settings of the generator model were default. The control system performed well without the presence of a constant power load in the model. But these simulations have shown that the generator voltage control system is strongly affected by the presence of a constant power load. Therefore the performance setting of the generator voltage control system is adjusted in this paragraph, to see the effect

on grid voltage oscillations. The voltage control performance is expressed in its frequency bandwidth. A bandwidth of 1PU is the default value, used in the simulations of the previous paragraph, reducing this figure means a reduction in the voltage control speed.

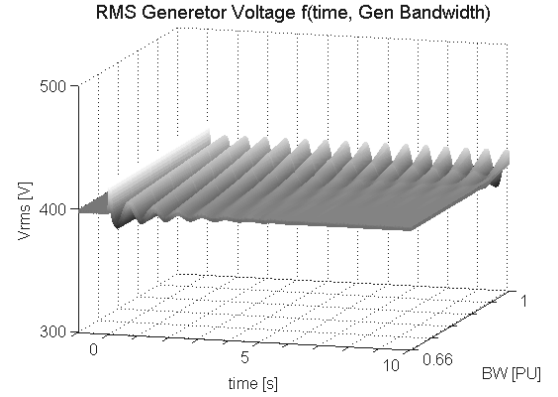


Figure 12, The effect of the generator voltage control bandwidth, while loaded with 100% constant power load.

As can be noticed in Figure 12, the generator voltage control bandwidth in this simulated set-up needs to be reduced only slightly to free the system from poorly damped oscillatory voltages. Most important here is the increase in gain and phase margin of the generators voltage control system.

VI. CONCLUSIONS

The combination of virtual reduction of parallel capacitances and virtual harmonic resistive damping of the inverters for DER is very effective to reduce resonances caused by the implementation of large numbers of inverters and modern appliances with power electronic converters. The effect can be on a large frequency range that includes a number of harmonics. The involved ancillary control system needs no estimating of the actual level of harmonics in the network. Beside this, there is no fear for instabilities involved with this system. Because the damping resistance is virtual, the energy involved is limited to small losses in the inverter. Another advantage of being virtual is that the only effort to be taken is an extension of the control system of the inverter, with this, costs can be kept minimal.

A disadvantage is that there is a contradiction in the needed measure for background harmonics, harmonics from the next higher network. Therefore the reduction must be limited to avoid wrong compensation, resulting in excessive harmonic currents through the distribution transformer and lines. However, a combination with a series active filter on substation level can solve this problem.

From simulation with Matlab/Simulink and laboratory measurements, the following conclusions can be made concerning oscillatory voltages for single and three-phase appliances with a significant buffer capacitor and constant power load behaviour. Interaction of the negative differential impedance of appliances and the voltage control

systems of generators can cause low frequency oscillatory voltages, especially in small isolated grids or distribution areas that operate in island mode. This is because the energy supply is small and easily affected by a constant power load share that takes the overhand. But it can also show up in larger electricity networks when the constant power load share remains growing at various power levels.

A solution for this problem can be found in changing the parameters of voltage control systems of generators to achieve more gain and phase margin.

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VIII. AUTHORS' BIOGRAPHIES



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